Docket No.

246575US6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Takahito NAKANO, et al.

SERIAL NO:

New Application

FILING DATE: Herewith

FOR:

SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS AND CIRCUIT BOARD AND

INFORMATION READOUT METHOD

## FEE TRANSMITTAL

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	8 - 20 =	0	x \$18 =	\$0.00
INDEPENDENT CLAIMS	4 - 3 =	1	x \$86 =	\$86.00
☐ MULTIPLE DEPENDEN	T CLAIMS (If appli	icable)	+ \$290 =	\$0.00
LATE FILING OF DECL	ARATION		+ \$130 =	\$130.00
BASIC FEE				\$770.00
	\$986.00			
☐ REDUCTION BY 50% FOR FILING BY SMALL ENTITY				\$0.00
☐ FILING IN NON-ENGLI	SH LANGUAGE		+ \$130 =	\$0.00
☐ RECORDATION OF AS	SIGNMENT		+ \$40 =	\$0.00
			TOTAL	\$986.00

	Please charge Deposit Account No. 15-0030 in the amount of \$0.00 A duplicate copy of this sheet is enclosed.				
	A check in the amount of \$0.00 to cover the filing fee is enclosed.				
	Credit card payment form is attached to cover the filing fee in the amount of \$986.00				
	The Director is hereby authorized to charge any additional fees which may be required for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.				
•	Respectfully Submitted,				
	OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.				
Da	12/16/03 Q/mmMGrllant				
	Bradley D. Lytle				
	Registration No. 40,073				
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C. Irvin McClelland Registration Number 21,124

Customer Number

Fax. (703) 413-2220 (OSMMN 05/03)